

INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)	Application Number		09633087
	Filing Date		2000-08-04
	First Named Inventor	Derrick R. Meyer	
	Art Unit	2157 2457	
	Examiner Name	Burgess, Barbara N.	
	Attorney Docket Number	5500-98300	

U.S.PATENTS						Remove
Examiner Initial*	Cite No	Patent Number	Kind Code ¹	Issue Date	Name of Patentee or Applicant of cited Document	Pages,Columns,Lines where Relevant Passages or Relevant Figures Appear
	1	6148300		2000-11-14	Singhal, et al.	
	2	4725834		1988-02-16	Change, et al.	
	3	4750109		1988-06-07	Kita	
	4	4807118		1989-02-21	Lin, et al.	
	5	4922408		1990-05-01	Davis, et al.	
	6	5055999		1991-10-08	Frank, et al.	
	7	5341483		1994-08-23	Frank, et al.	
	8	5351043		1994-09-27	Hullett, et al.	

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**
(Not for submission under 37 CFR 1.99)

Application Number		09633087
Filing Date		2000-08-04
First Named Inventor	Derrick R. Meyer	
Art Unit		2157
Examiner Name	Burgess, Barbara N.	
Attorney Docket Number		5500-98300

	9	5495481		1996-02-27	Duckwall	
	10	5502840		1996-03-26	Barton	
	11	5506961		1996-04-09	Carlson, et al.	
	12	5548728		1996-08-20	Danknick	
	13	5592486		1997-01-07	Lo, et al.	
	14	5860159		1999-01-12	Hagersten	
	15	5875485		1999-02-23	Matsumoto	
	16	5175837		1992-12-29	Arnold, et al.	
	17	5579504		1996-11-26	Callander, et al.	
	18	5649157		1997-07-15	Williams	
	19	5657472		1997-08-12	Van Loo, et al.	

INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)

Application Number	09633087
Filing Date	2000-08-04
First Named Inventor	Derrick R. Meyer
Art Unit	2157
Examiner Name	Burgess, Barbara N.
Attorney Docket Number	5500-98300

If you wish to add additional U.S. Patent citation information please click the Add button. [Add](#)

U.S.PATENT APPLICATION PUBLICATIONS [Remove](#)

Examiner Initial*	Cite No	Publication Number	Kind Code ¹	Publication Date	Name of Patentee or Applicant of cited Document	Pages,Columns,Lines where Relevant Passages or Relevant Figures Appear
	1					

If you wish to add additional U.S. Published Application citation information please click the Add button. [Add](#)

FOREIGN PATENT DOCUMENTS [Remove](#)

Examiner Initial*	Cite No	Foreign Document Number ³	Country Code ²	Kind Code ⁴	Publication Date	Name of Patentee or Applicant of cited Document	Pages,Columns,Lines where Relevant Passages or Relevant Figures Appear	T ⁵
	1							<input type="checkbox"/>

If you wish to add additional Foreign Patent Document citation information please click the Add button [Add](#)

NON-PATENT LITERATURE DOCUMENTS [Remove](#)

Examiner Initials*	Cite No	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc), date, pages(s), volume-issue number(s), publisher, city and/or country where published.	T ⁵
	1	Dijkstra, E.W., "Solution of a Problem in Concurrent Programming Control," Communications of the ACM, vol. 8, No. 9, Sep., 1965, p. 569.	<input type="checkbox"/>
	2	Gustavson, David B., "The Scalable Coherent Interface and Related Standards Projects," IEEE Micro, 1992, p. 10.	<input type="checkbox"/>
	3	Cox, et al., "Adaptive Cache Coherency for Detecting Migratory Shared Data," Proc. 20th Annual Symposium on Computer Architecture, May 1993, pp. 98-108.	<input type="checkbox"/>
	4	Stenstrom, et al., "An Adaptive Cache Coherence Protocol Optimized for Migratory Sharing," Proc. 20.h Annual Symposium on Computer Architecture, May 1993 IEEE, pp. 109-118.	<input type="checkbox"/>

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**
(Not for submission under 37 CFR 1.99)

Application Number	09633087
Filing Date	2000-08-04
First Named Inventor	Derrick R. Meyer
Art Unit	2157
Examiner Name	Burgess, Barbara N.
Attorney Docket Number	5500-98300

5	Wolf-Dietrich Weber, et al. "Analysis of Cache Invalidation Patterns in Multiprocessors", Computer Systems Laboratory, Stanford University, CA, pp. 243-256.	<input type="checkbox"/>
6	Kourosh, et al. "Two Techniques to Enhance the Performance of Memory Consistency Models," 1991 International Conference on Parallel Processing, pp. 1-10.	<input type="checkbox"/>
7	Li, et al. "Memory Coherence in Shared Virtual Memory Systems," 1986, Proceedings of the fifth annual ACM symposium on Principles of distributed computing ACM, pp. 229-239.	<input type="checkbox"/>
8	Magnusson, et al., "Queue Locks on Cache Coherent Multiprocessors", IEEE, In Proceedings of the 8th International Symposium on Parallel Processing 1994.	<input type="checkbox"/>
9	Hagersten, et al., "Simple COMA," Ashley Saulsbury and Anders Landin Swedish Institute of Computer Science, Jul. 1993, pp. 233-259.	<input type="checkbox"/>
10	Saulsbury, et al., "An Argument for Simple COMA," Swedish Institute of Computer Science, 10 pages.	<input type="checkbox"/>
11	Anderson, "The Performance of Spin Lock Alternatives for Shared-memory Multiprocessors", IEEE Transactions on Parallel and Distributed Systems, vol. 1, No. 1, Jan. 1990.	<input type="checkbox"/>

If you wish to add additional non-patent literature document citation information please click the Add button

EXAMINER SIGNATURE

Examiner Signature	/Barbara Burgess/	Date Considered	08/14/2009
--------------------	-------------------	-----------------	------------

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through a citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ See Kind Codes of USPTO Patent Documents at www.USPTO.GOV or MPEP 901.04. ² Enter office that issued the document, by the two-letter code (WIPO Standard ST.3). ³ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁴ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. ⁵ Applicant is to place a check mark here if English language translation is attached.